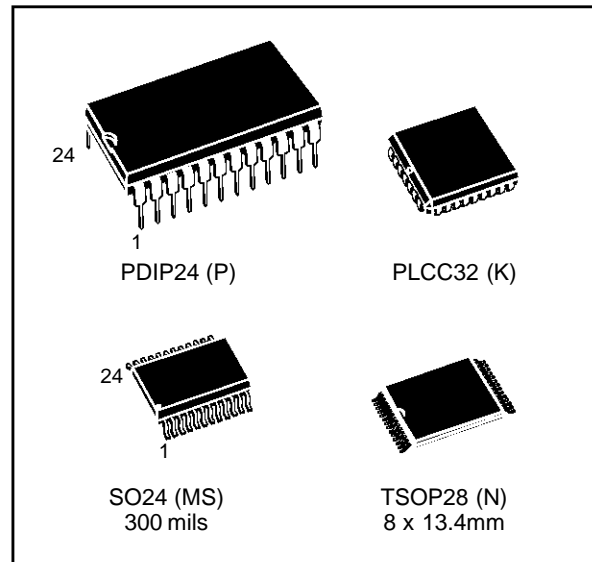


LOW VOLTAGE PARALLEL ACCESS 16K (2K x 8) EEPROM WITH SOFTWARE DATA PROTECTION

DATA BRIEFING

- FAST ACCESS TIME: 200ns
- SINGLE LOW VOLTAGE OPERATION
- LOW POWER CONSUMPTION
- FAST WRITE CYCLE:
 - 64 Bytes Page Write Operation
 - Byte or Page Write Cycle: 3ms Max
- ENHANCED END OF WRITE DETECTION:
 - Data Polling
 - Toggle Bit
- PAGE LOAD TIMER STATUS BIT
- HIGH RELIABILITY SINGLE POLYSILICON, CMOS TECHNOLOGY:
 - Endurance >100,000 Erase/Write Cycles
 - Data Retention >40 Years
- JEDEC APPROVED BYTEWIDE PIN OUT
- SOFTWARE DATA PROTECTION



DESCRIPTION

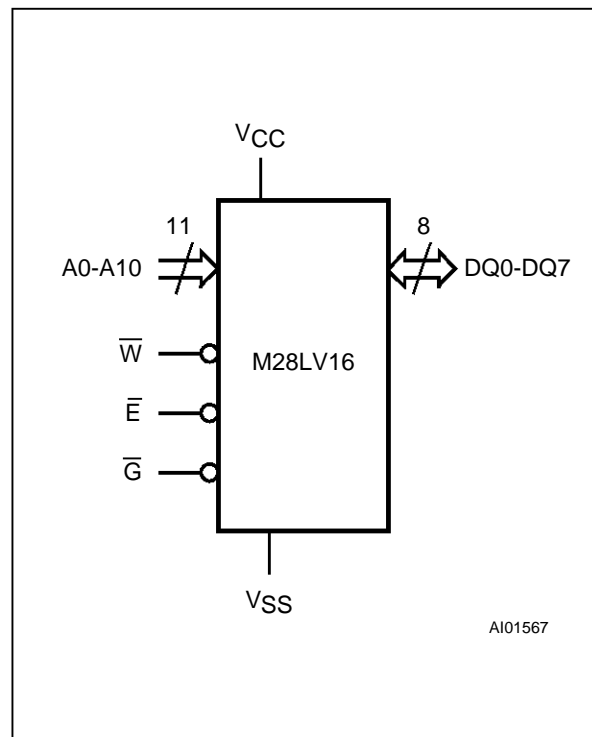
The M28LV16 is a 2K x 8 low power Parallel EEPROM fabricated with SGS-THOMSON proprietary single polysilicon CMOS technology. The device offers fast access time with low power dissipation and requires a 2.7V to 3.6V power supply.

The circuit has been designed to offer a flexible microcontroller interface featuring both hardware and software handshaking with Data Polling and Toggle Bit. The M28LV16 supports 64 byte page write operation. A Software Data Protection (SDP) is also possible using the standard JEDEC algorithm.

Signal Names

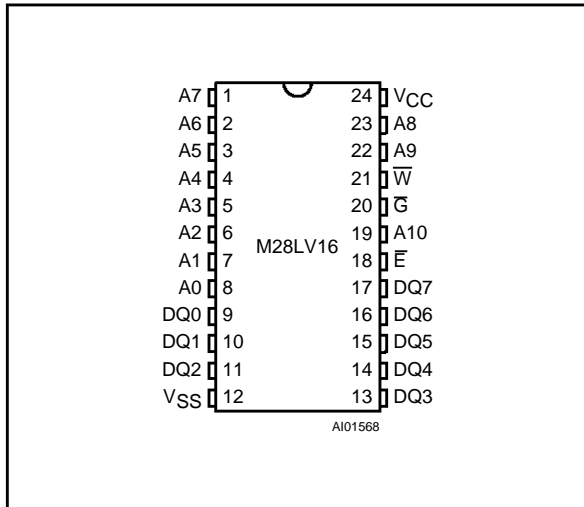
A0 - A10	Address Input
DQ0 - DQ7	Data Input / Output
\bar{W}	Write Enable
\bar{E}	Chip Enable
\bar{G}	Output Enable
V _{CC}	Supply Voltage
V _{SS}	Ground

Logic Diagram

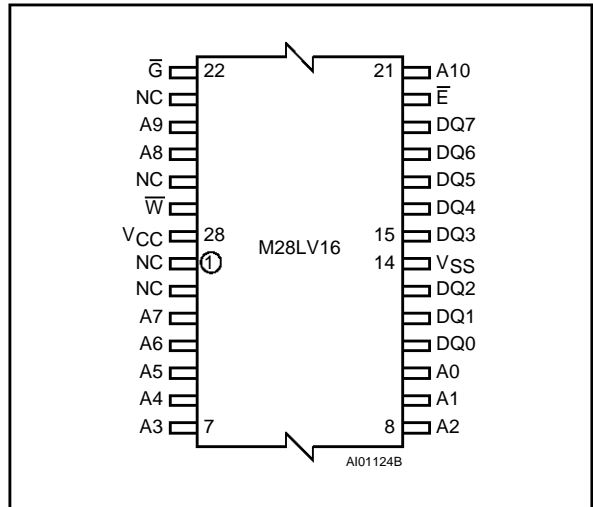


M28LV16

DIP Pin Connections

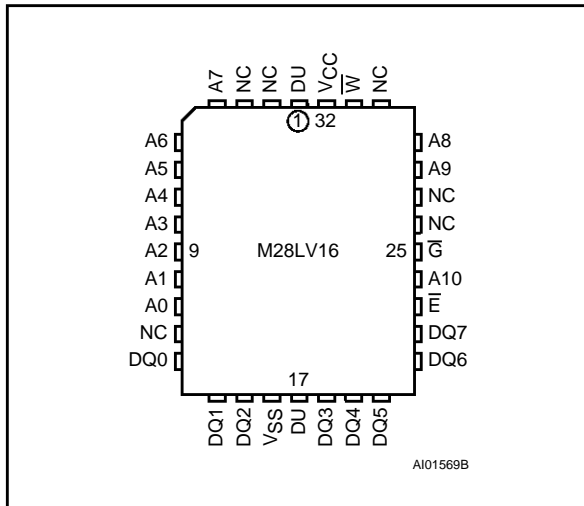


TSOP Pin Connections



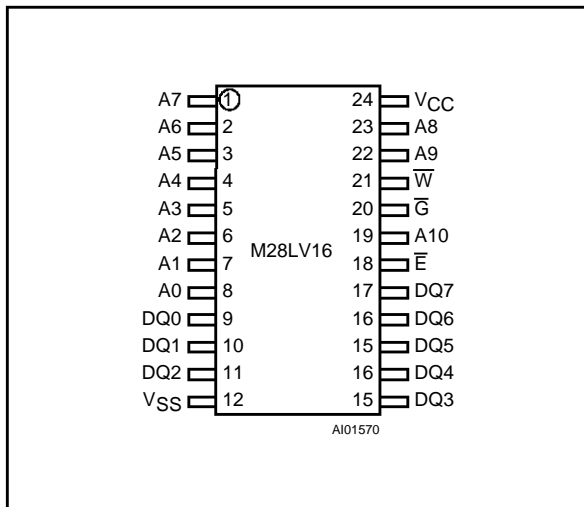
Warning: NC = Not Connected.

LCC Pin Connections



Warning: NC = Not Connected, DU = Don't Use.

SO Pin Connections



Ordering Information Scheme

For a list of available options refer to the current Memory Shortform catalogue.

For further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.

Example: M28LV16 -200 K 1

Speed	-200	200ns
	-250	250ns
	-300	300ns
Package	P	PDIP24
	K	PLCC32
	MS	SO24 300mils
	N	TSOP28 8 x 13.4mm
Temp. Range	1	0 to 70 °C
	6	-40 to 85 °C